

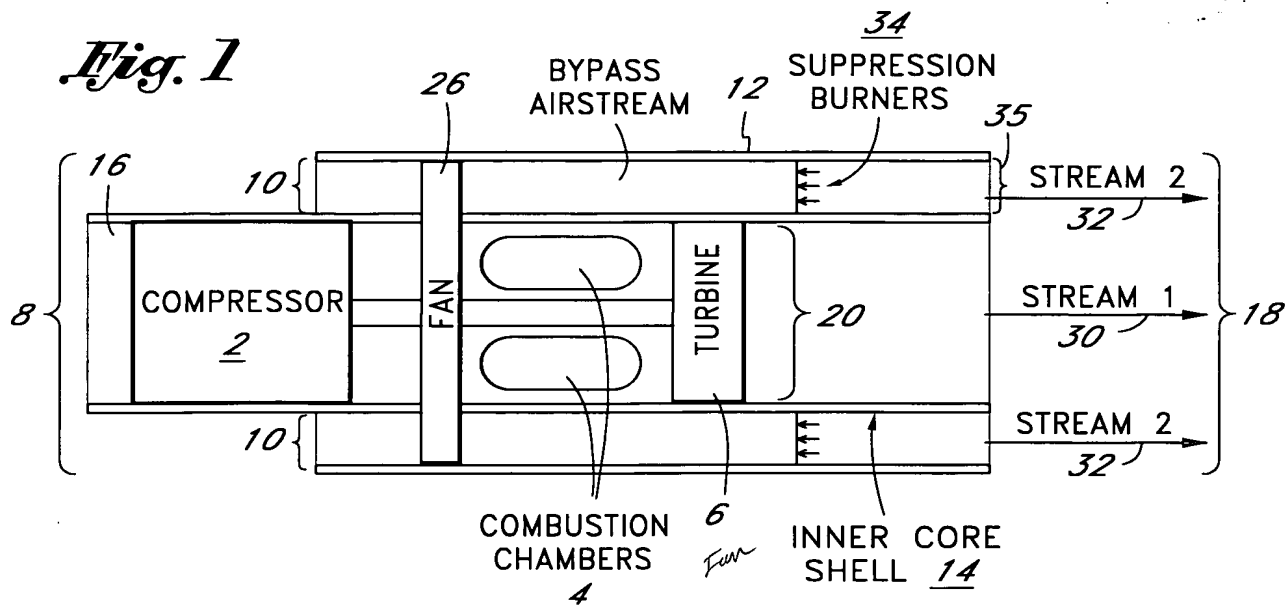
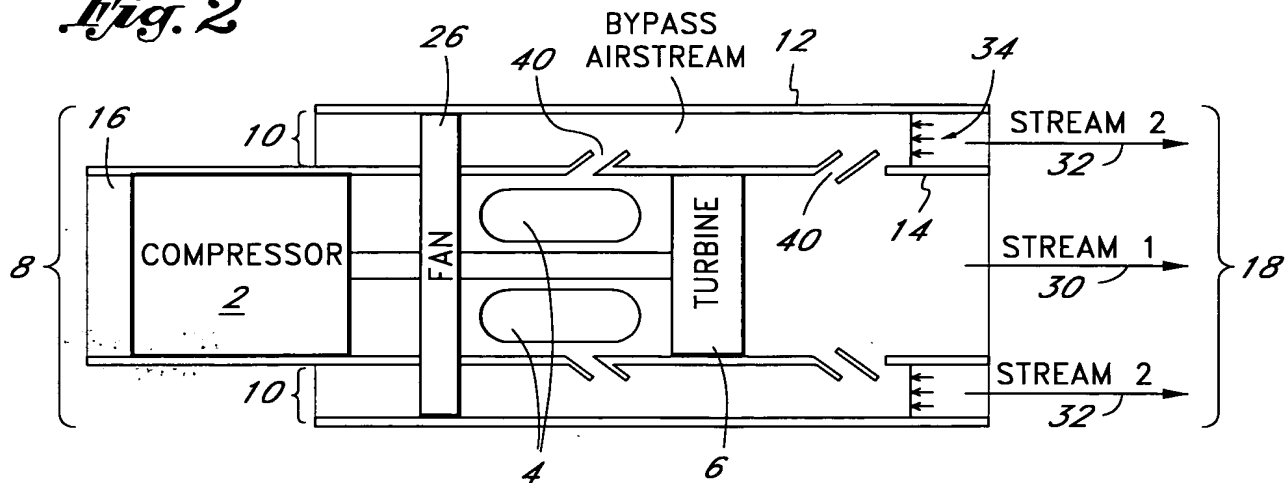
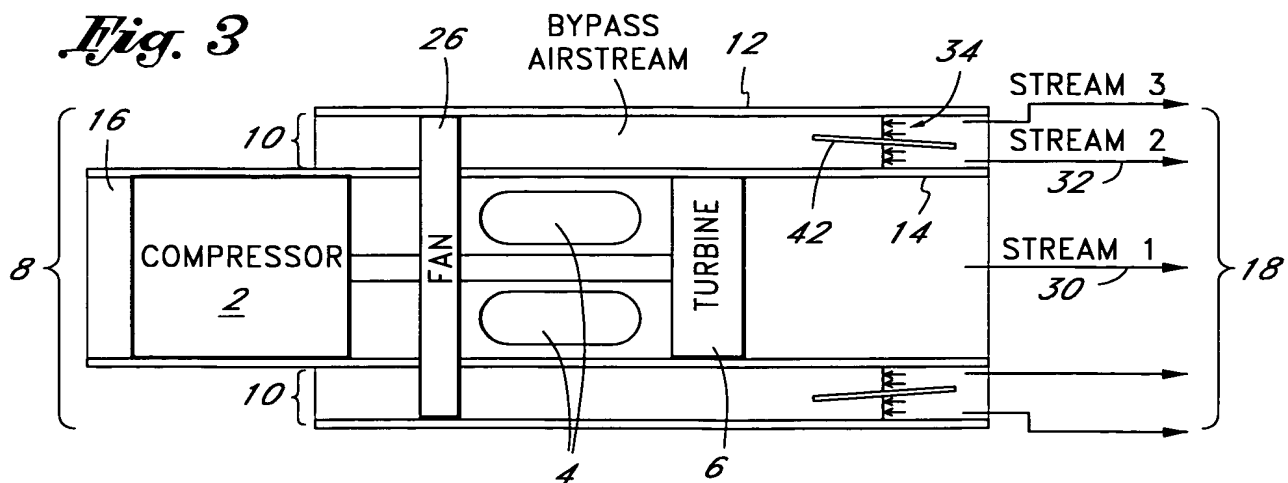
Fig. 1*Fig. 2**Fig. 3*

Fig. 4

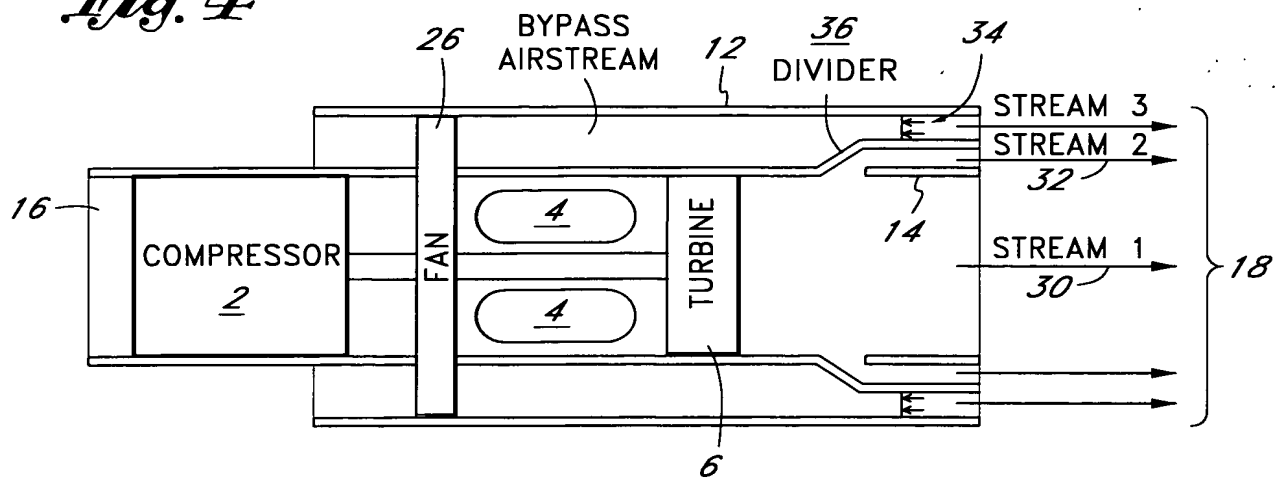


Fig. 5

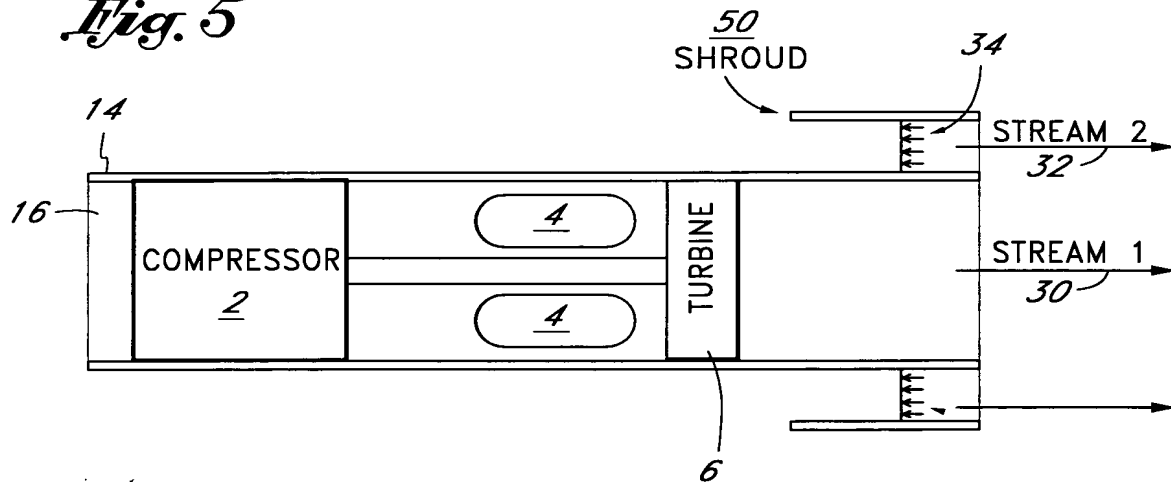


Fig. 6

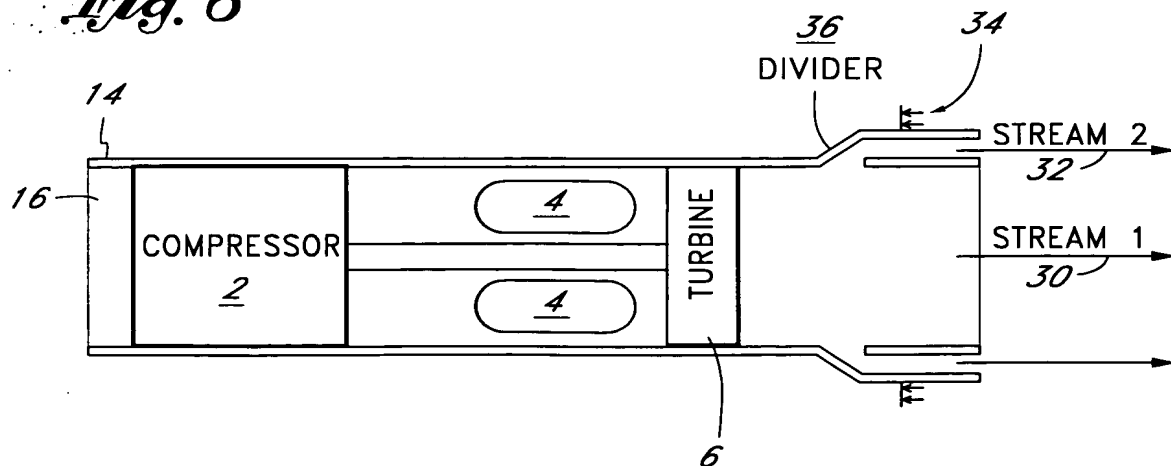


Fig. 7A

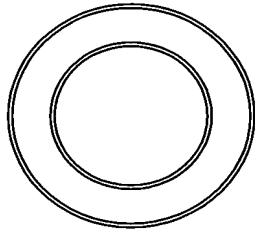


Fig. 7B

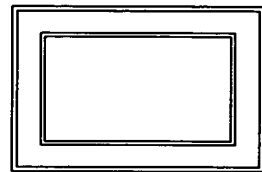


Fig. 8A

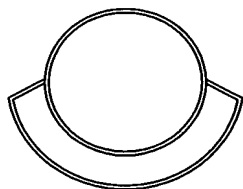


Fig. 8B

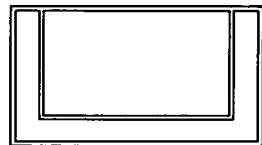


Fig. 8C

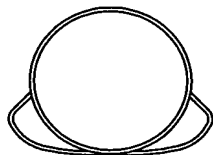


Fig. 8D

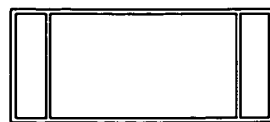


Fig. 9A

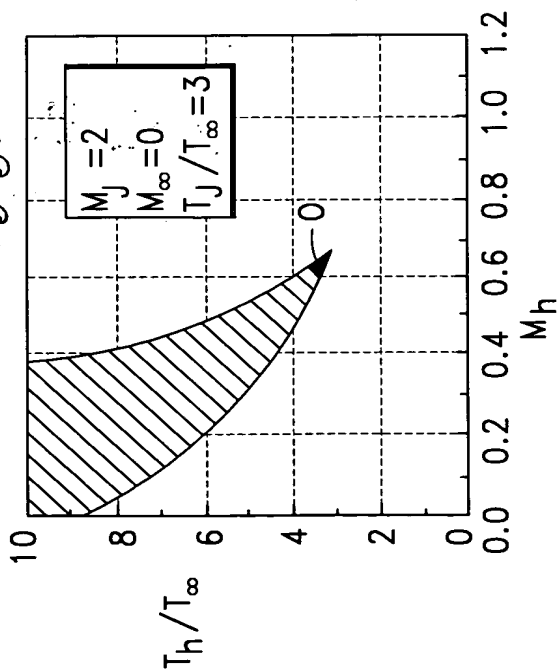


Fig. 9C

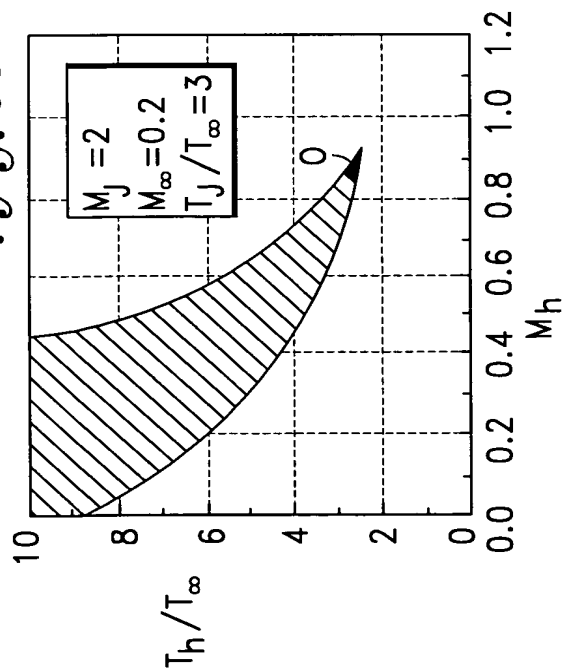


Fig. 9B

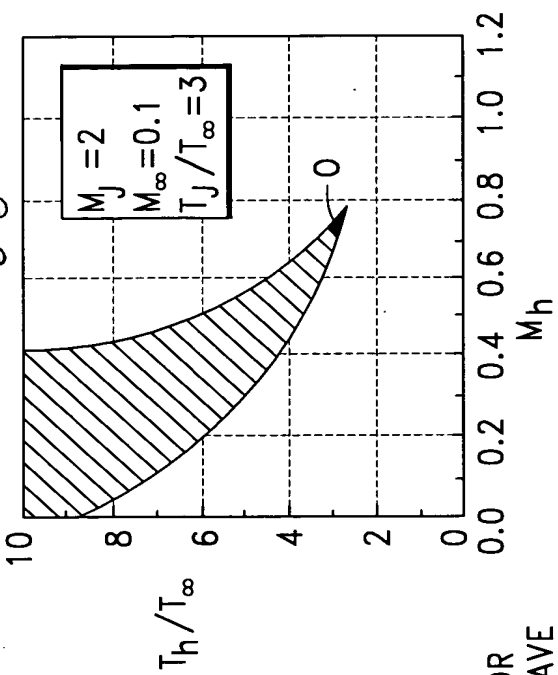
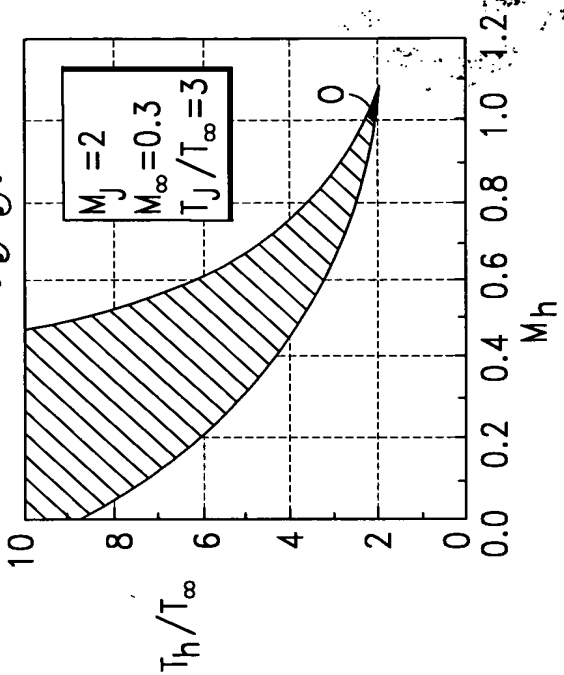


Fig. 9D



AREA FOR
 MOCK WAVE
 SUPPRESSION

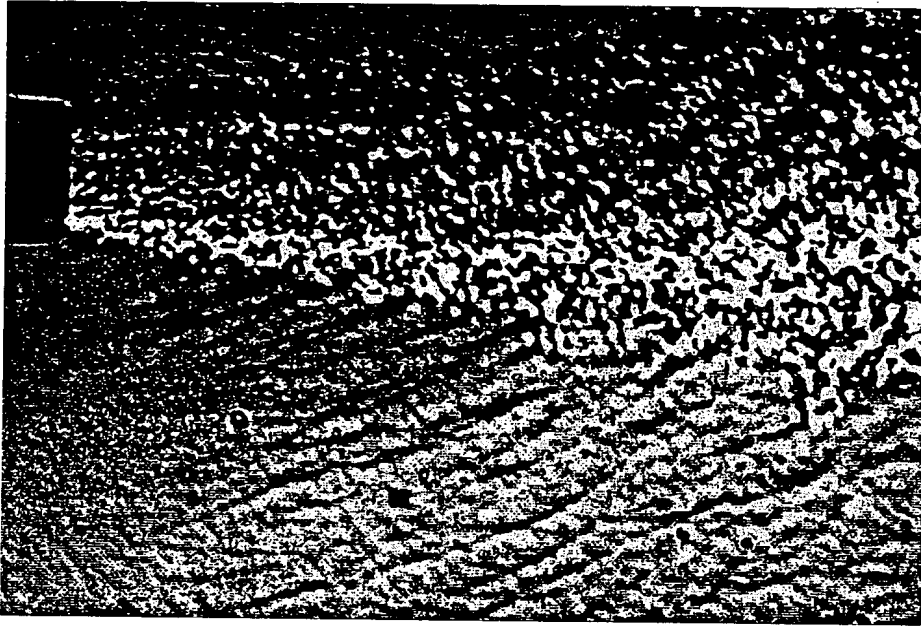


FIG.10A

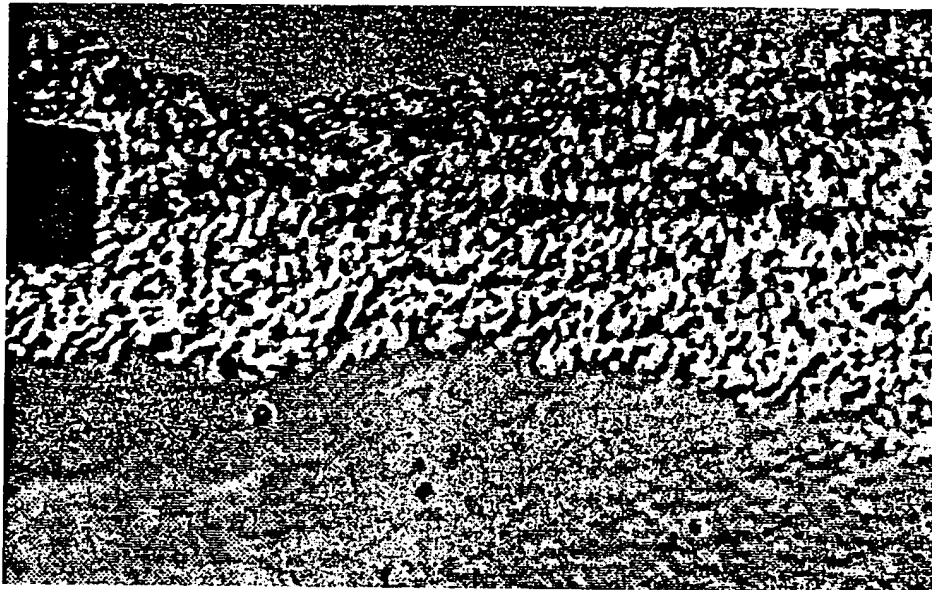


FIG.10B